

JST80IS-1600BW 80A TRIAC

Rev.A.1.0

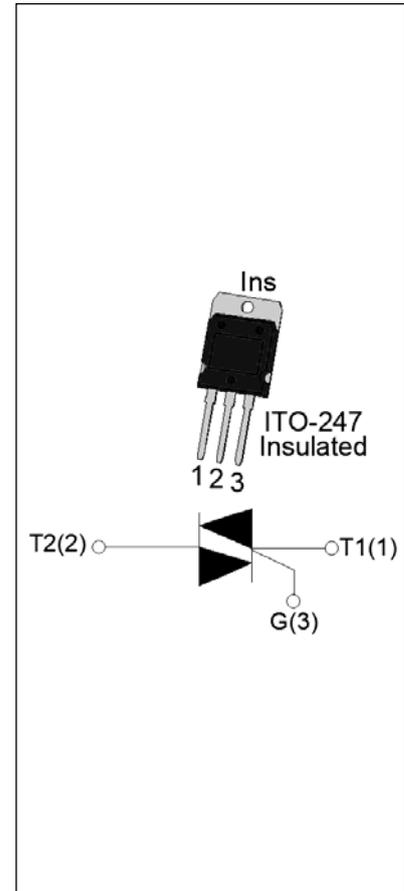
DESCRIPTION:

The JST80IS-1600WB triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers.

JST80IS-1600WB snubberless triac is especially recommended for use on inductive loads. By using an internal ceramic pad, JST80IS-1600WB provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package ITO-247 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	80	A
V_{DRM}/V_{RRM}	1600	V
$I_{GT\ I/II/III}$	50/50/50	mA


ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-125	°C
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	1600	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	1600	V
RMS on-state current ($T_c \leq 90^\circ\text{C}$)	$I_{T(RMS)}$	80	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	800	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		880	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	3200	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	100	$\text{A}/\mu\text{s}$

Peak gate current (tp=20μs , Tj=125°C)	I _{GM}	10	A
Average gate power dissipation (Tj=125°C)	P _{G(AV)}	0.5	W
Peak gate power	P _{GM}	25	W
Peak pulse voltage (Tj=25°C; non-repetitive, off-state; FIG.7)	V _{pp}	1	kV

ELECTRICAL CHARACTERISTICS(Tj=25°C unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33Ω	I - II -III	MAX.	50	mA
V _{GT}		I - II -III	MAX.	1.3	V
V _{GD}	V _D =V _{DRM} Tj=125°C R _L =3.3KΩ	I - II -III	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	I -III	MAX.	80	mA
		II		120	
I _H	I _T =1A		MAX.	70	mA
dV/dt	V _D =1070V Gate Open Tj=125°C		MIN.	1500	V/μs
(dI/dt) _c	(dV/dt) _c =20V/μs Tj=125°C		MIN.	28	A/ms
t _{on}	I _G =80mA I _A =400mA I _R =40mA Tj=25°C		TYP.	12	μs
t _{off}				80	

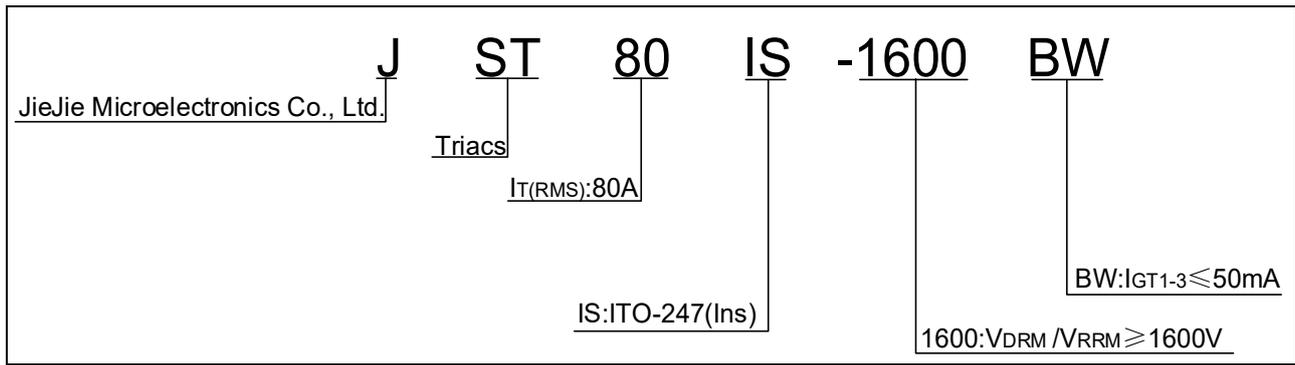
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V _{TM}	I _{TM} =120A tp=380μs	Tj=25°C	1.9	V
V _{TO}	Threshold voltage	Tj=125°C	0.71	V
R _D	Dynamic resistance	Tj=125°C	23	mΩ
I _{DRM}	V _D =V _{DRM} V _R =V _R RM	Tj=25°C	20	μA
I _R RM		Tj=125°C	12	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R _{th(j-c)}	junction to case (AC)	0.27	°C/W
R _{th(j-a)}	junction to ambient (AC)	45	°C/W

ORDERING INFORMATION



MARKING

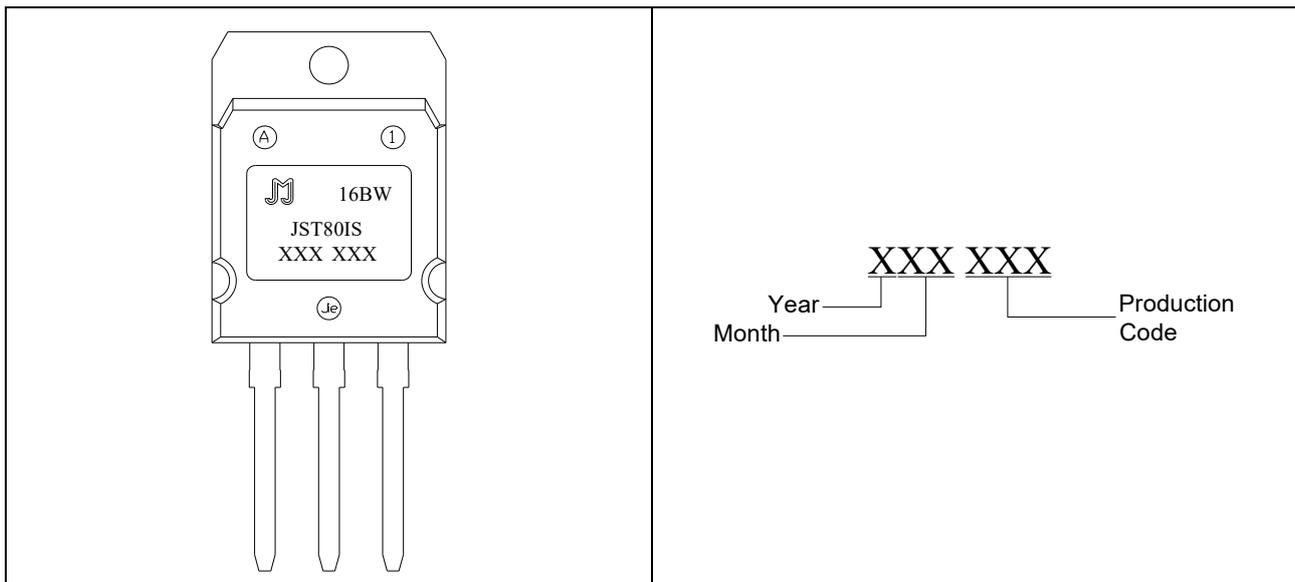


FIG.1 Maximum power dissipation versus RMS on-state current

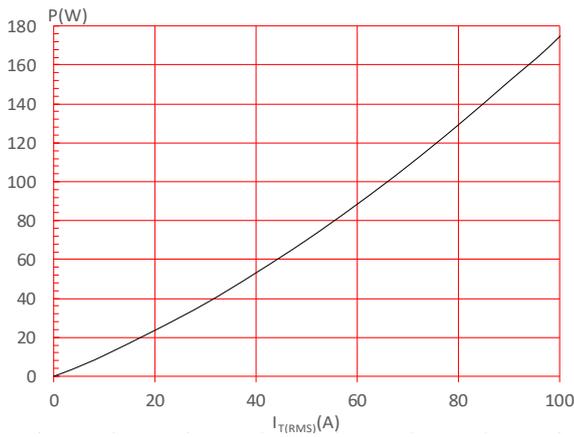


FIG.2: RMS on-state current versus case temperature

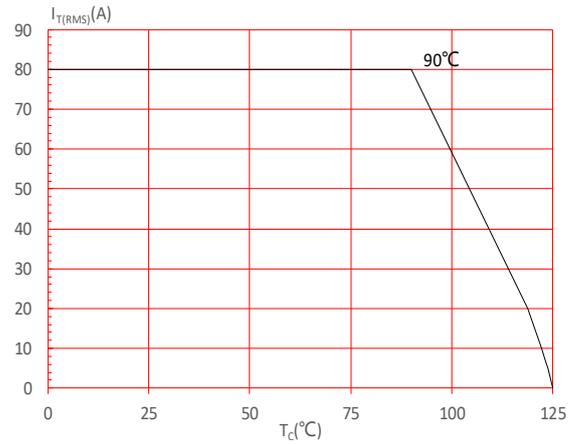


FIG.3: Surge peak on-state current versus number of cycles

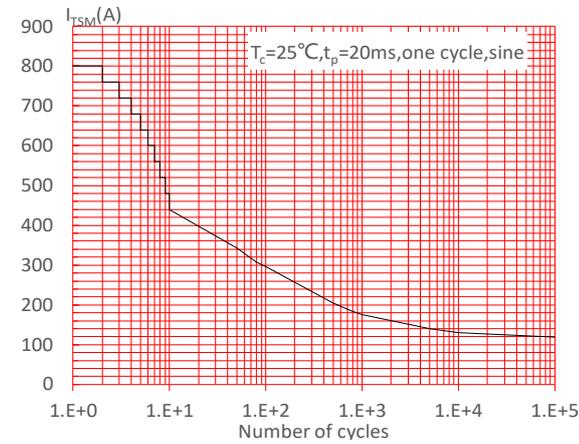


FIG.4: On-state characteristic

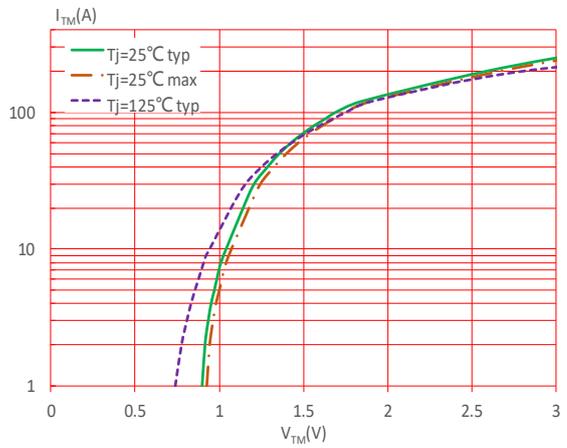


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($di/dt < 100\text{A}/\mu\text{s}$)

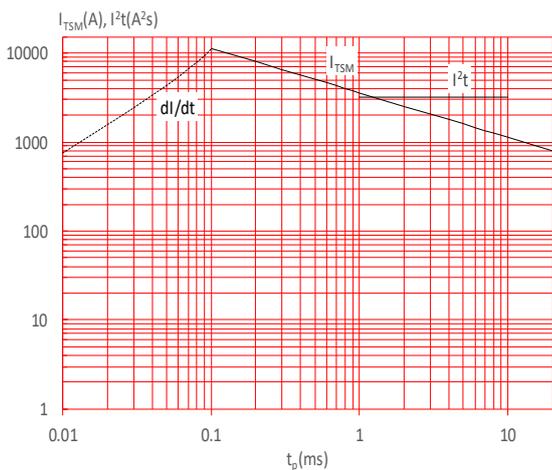


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

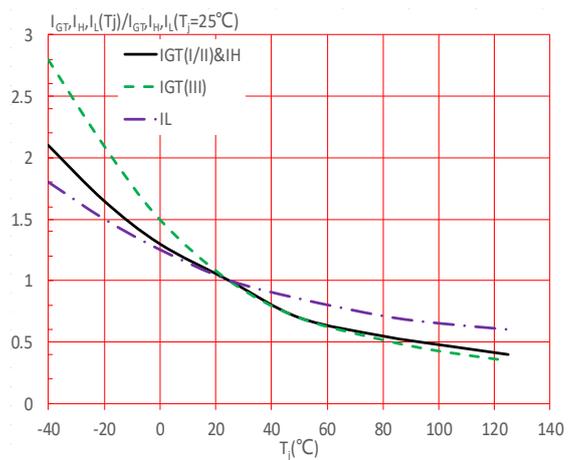
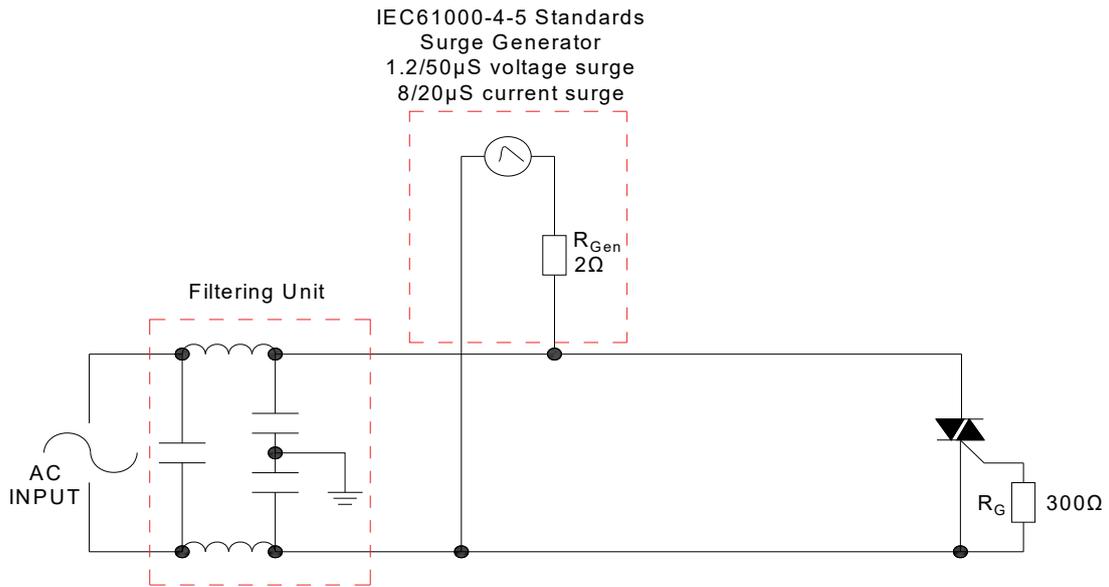


FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



SHAPING AND SOLDERING PARAMETERS

Refer to 《Instructions for installation of plastic-sealed in-line power devices》 released by JieJie

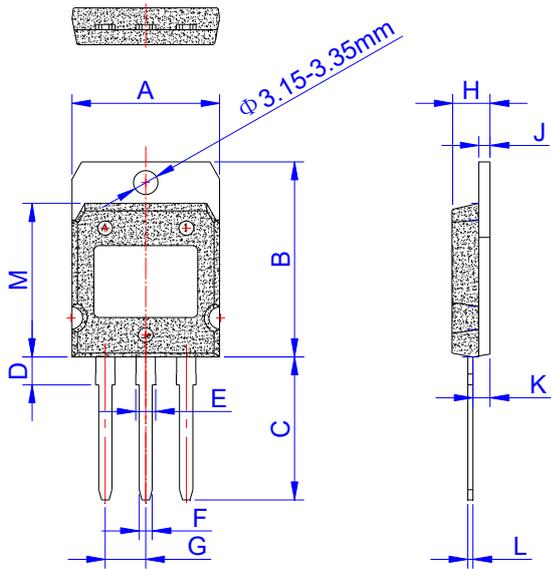
ORDERING INFORMATION

Order code	Voltage $V_{DRM}/V_{RRM}(V)$	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		I - II - III			
JST80IS-1600BW	1600	50	ITO-247(Ins)	25	Tube

Document Revision History

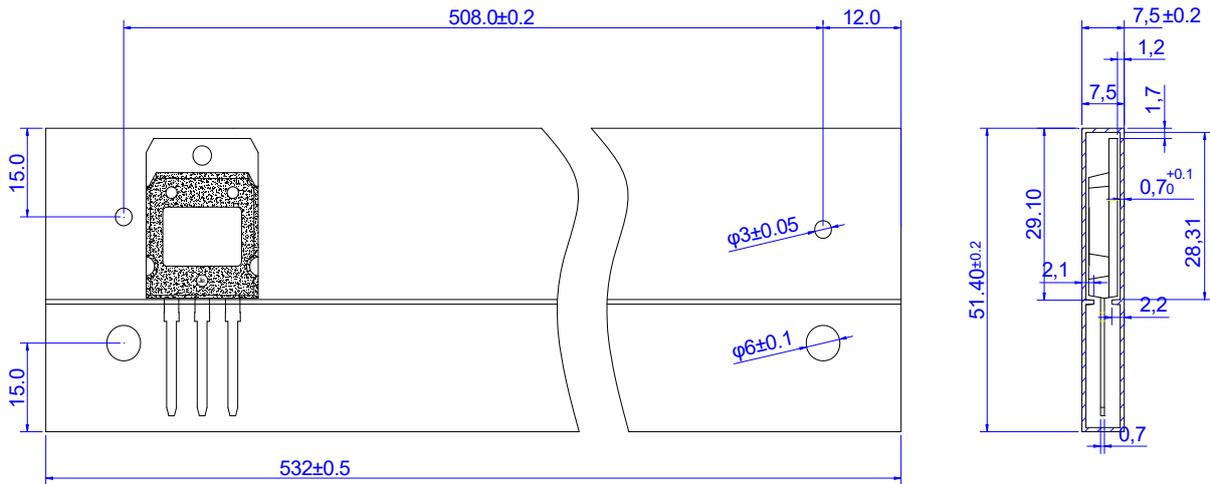
Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	19.7	19.9	20.1	0.776	0.783	0.791
B	26.9	27.1	27.3	1.059	1.067	1.075
C	19.4	19.9	20.4	0.764	0.783	0.803
D	3.80	3.90	4.00	0.150	0.154	0.157
E	2.56	2.66	2.76	0.101	0.105	0.109
F	1.66	1.76	1.86	0.065	0.069	0.073
G	5.25		5.65	0.207		0.222
H	5.05	5.10	5.50	0.199	0.201	0.217
J	1.45	1.50	1.55	0.057	0.059	0.061
K	2.20	2.30	2.40	0.087	0.091	0.094
L	0.60	0.70	0.80	0.024	0.028	0.031
M	21.2	21.3	21.4	0.835	0.839	0.843

DELIVERY MODE



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
ITO-247	TUBE	25	400	1,600

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